



HUIZHOU LCDIND TECHNOLOGY Co., LTD.

PRODUCT SPECIFICATION

7" SQUARE LCD MODULE

MODEL NAME: DBC070LCDAXN20L050A

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FOR CUSTOMER	
CUSTOMER APPROVED	

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Record of Revision

Version and Date	Page	Old description	New Description
1.0 20240110	23	initial release	

1. Operating Precautions

- 1) Since front polarizer is easily damaged, please be cautious and not to scratch it.
- 2) Be sure to turn off power supply when inserting or disconnecting from input connector.
- 3) Wipe off water drop immediately. Long contact with water may cause discoloration or spots.
- 4) When the panel surface is soiled, wipe it with absorbent cotton or soft cloth.
- 5) Since the panel is made of glass, it may be broken or cracked if dropped or bumped on hard surface.
- 6) Since CMOS LSI is used in this module, take care of static electricity and insure human earth when handling.
- 7) Do not open nor modify the module assembly.
- 8) Do not press the reflector sheet at the back of the module to any direction.
- 9) In case if a module has to be put back into the packing container slot after it was taken out from the container, do not press the center of the LED Reflector edge. Instead, press at the far ends of the LED Reflector edge softly. Otherwise the TFT Module may be damaged.
- 10) At the insertion or removal of the Signal Interface Connector, be sure not to rotate nor tilt the Interface Connector of the TFT Module.
- 11) TFT-LCD Module is not allowed to be twisted & bent even force is added on module in a very short time. Please design your display product well to avoid external force applying to module by end-user directly.
- 12) Small amount of materials without flammability grade are used in the TFT-LCD module. The TFT-LCD module should be supplied by power complied with requirements of Limited Power Source (IEC60950 or UL1950), or be applied exemption.
- 13) Severe temperature condition may result in different luminance, response time.
- 14) Continuous operating TFT-LCD Module under high temperature environment may accelerate LED light bar exhaustion and reduce luminance dramatically.
- 15) The data on this specification sheet is applicable when TFT-LCD module is placed in landscape position.
- 16) Continuous displaying fixed pattern may induce image sticking. It's recommended to use screen saver or moving content periodically if fixed pattern is displayed on the screen.

2. General Description

DBC070AXN20L050A is cut LCD from 1024*768.

Original LCD is a Color Active Matrix Liquid Crystal Display composed of a TFT-LCD display. The screen format is intended to support WUXGA (1024(H) x 768(V)) screen and 16.7M (RGB 6-bits) colors. All input signals are LVDS interface compatible. All design rules of this module can correspond to PSWG standard.

2.1 Display Characteristics

The following items are characteristics summary on the table under 25 °C condition:

Items	Unit	Specifications
Screen Diagonal	[inch]	7
Active Area	[mm]	127.8x127.8
Outline Dimension	[mm]	145x145x10.29(max)
Pixel Pitch	[mm]	0.1665 x 0.1665
Pixel Arrangement		R.G.B. Vertical Stripe
Display Mode		Transmissive mode / Normally black
Nominal Input Voltage VDD	[Volt]	3.3 typ.
Luminance, White		1000 typ
Typical Power Consumption	[Watt]	
Viewing Angle (CR>10)		Viewing angle free (R/L 178 (Min.), U/D 178 (Min.))
Weight	[Grams]	350g (max.)
Physical Size	[mm]	
Electrical Interface		1 channel LVDS
Surface Treatment		Hard-coating (3H), Glare treatment
Support Color		16.7 M colors
Temperature Range Operating Storage (Non-Operating)	°C °C	
RoHS Compliance		RoHS Compliance

2.2 Optical Characteristics

The optical characteristics are measured under stable conditions at 25°C (Room Temperature):

Item	Unit	Conditions	Min.	Typ.	Max.	Note
White Luminance	[cd/m ²]	(center point)		1000		1
Uniformity	%	9 Points	70	85	-	1, 2, 3
Contrast Ratio			1000	1200	-	4
Cross talk	%					
Response Time	[msec]	Rising	-	5.7		6
	[msec]	Falling	-	2.3		
	[msec]	Raising + Falling		8		
Viewing Angle	[degree] [degree]	Horizontal (Right) CR = 10 (Left)	89 89	89 89	-	7
	[degree] [degree]	Vertical (Upper) CR = 10 (Lower)	89 89	89 89	- -	
Color / Chromaticity Coordinates (CIE 2931)		Red x		TBD		
		Red y		TBD		
		Green x		TBD		
		Green y		TBD		
		Blue x		TBD		
		Blue y		TBD		
		White x		TBD		
		White y		TBD		
Color Gamut	%			72	-	

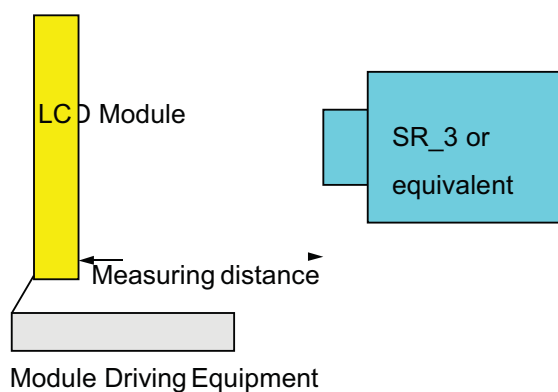
Note 1: Measurement method

Equipment Pattern Generator, Power Supply, Digital Voltmeter, Luminance meter (SR_3 or equivalent)

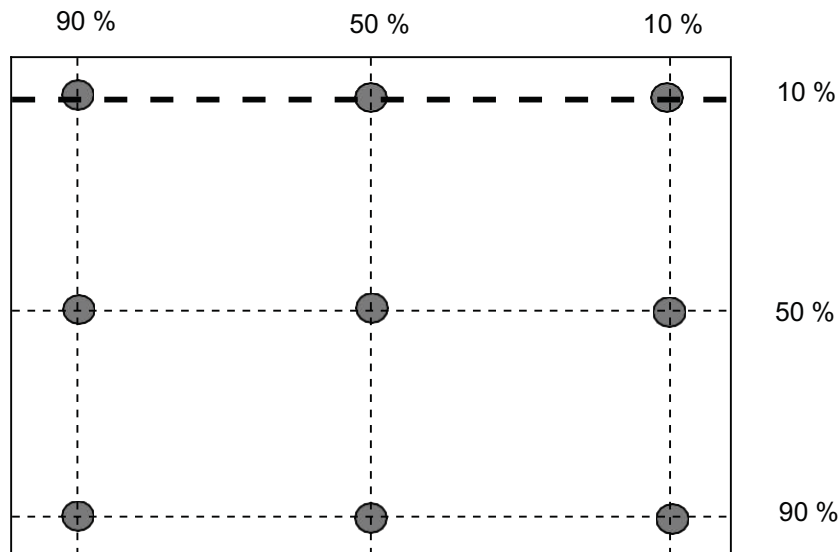
Aperture 1" with 50cm viewing distance

Test Point Center

Environment < 1 lux



Note 2: Definition of 9 points position (Display active area : 170.4(H) x 127.8(V))



Note 3: The luminance uniformity of 9 points is defined by dividing the minimum luminance values by the maximum test point luminance

$$\delta_{w9} = \frac{\text{Minimum Brightness of nine points}}{\text{Maximum Brightness of nine points}}$$

Note 4: Definition of contrast ratio (CR):

$$\text{Contrast ratio (CR)} = \frac{\text{Brightness on the "White" state}}{\text{Brightness on the "Black" state}}$$

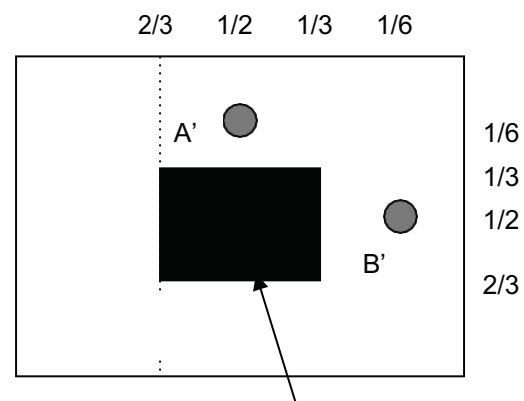
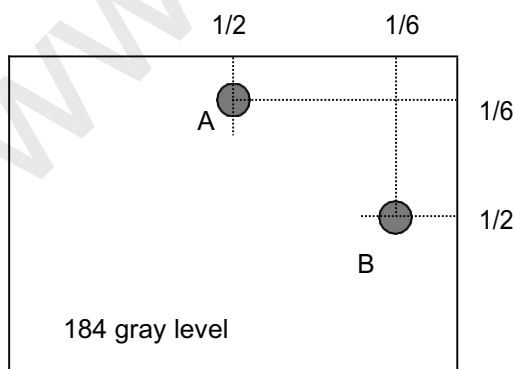
Note 5: Definition of cross talk (CT)

$$CT = |YB - YA| / YA \times 100 (\%)$$

Where

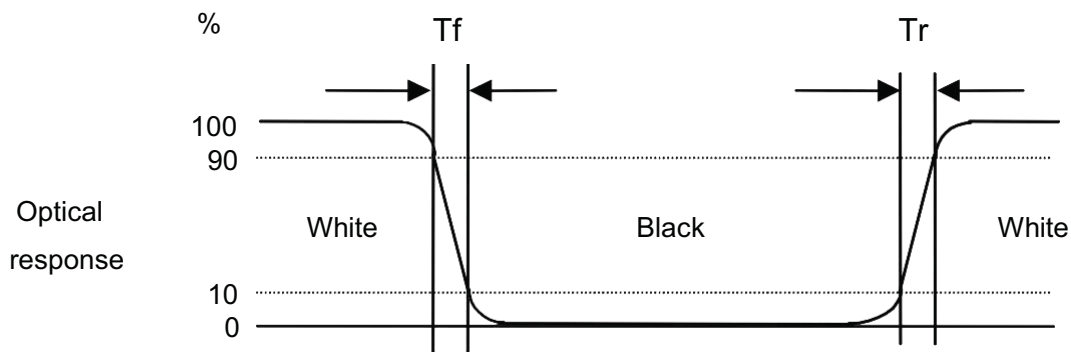
YA = Luminance of measured location without gray level 0 pattern (cd/m2)

YB = Luminance of measured location with gray level 0 pattern (cd/m2)



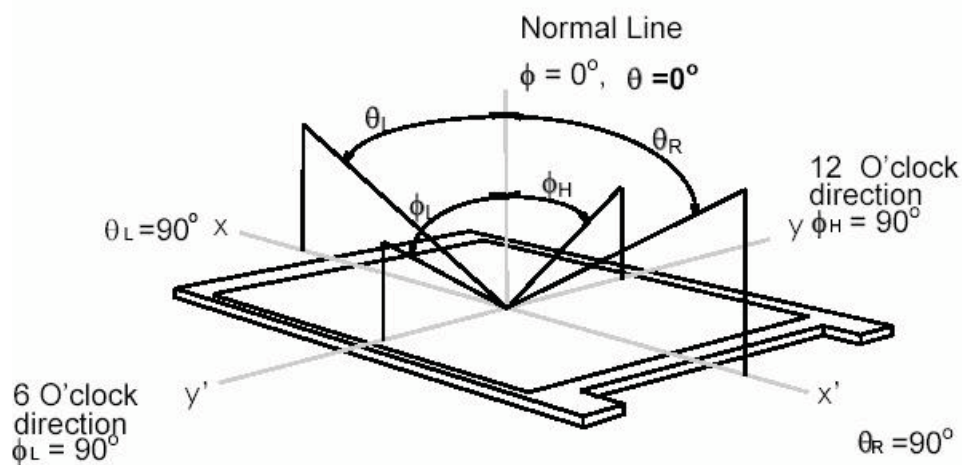
Note 6: Definition of response time:

The output signals of photo detector are measured when the input signals are changed from “White” to “Black” (falling time) and from “Black” to “White” (rising time), respectively. The response time interval is between 10% and 90% of amplitudes. Please refer to the figure as below.



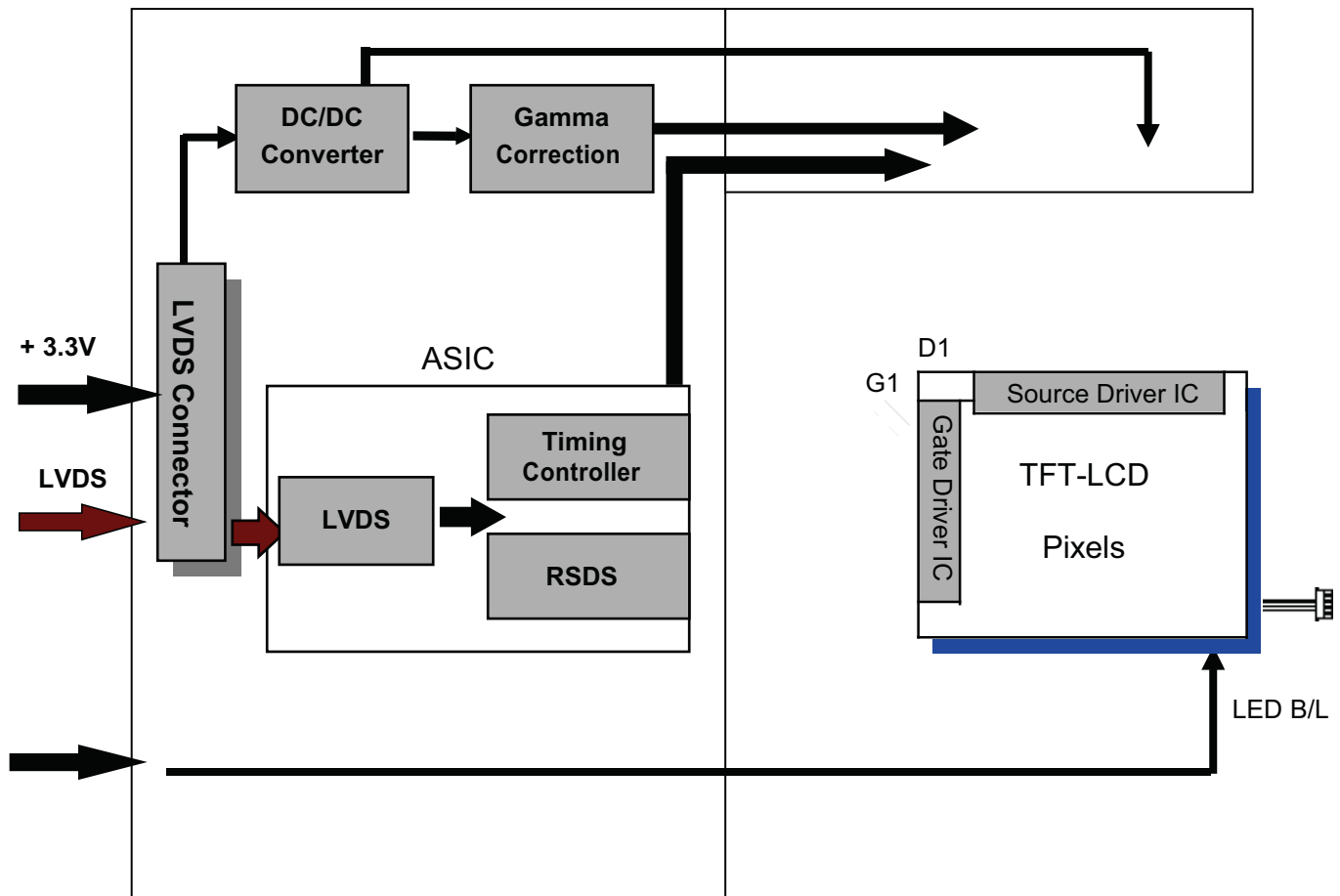
Note 7: Definition of viewing angle

Viewing angle is the measurement of contrast ratio ≥ 10 , at the screen center, over a 180° horizontal and 180° vertical range (off-normal viewing angles). The 180° viewing angle range is broken down as below: $90^\circ(\theta)$ horizontal left and right, and $90^\circ(\Phi)$ vertical high (up) and low (down). The measurement direction is typically perpendicular to the display surface with the screen rotated to its center to develop the desired measurement viewing angle.



3. Functional Block Diagram

The following diagram shows the functional block of the TFT/LCD module:



4. Absolute Maximum Ratings

4.1 Absolute Ratings of TFT LCD Module

Item	Symbol	Min	Max	Unit
Logic/LCD Drive Voltage	Vin	- 0.3	+3.6	[Volt]

4.2 Absolute Ratings of Environment

Item	Symbol	Min	Max	Unit
Operating Temperature	TOP	-20	70	[°C]
Operation Humidity	HOP	8	90	[%RH]
Storage Temperature	TST	-30	+80	[°C]
Storage Humidity	HST	8	90	[%RH]

Note: Maximum Wet-Bulb should be 39°C and no condensation.

5. Electrical Characteristics

5.1 TFT LCD Module

5.1.1 Power Specification

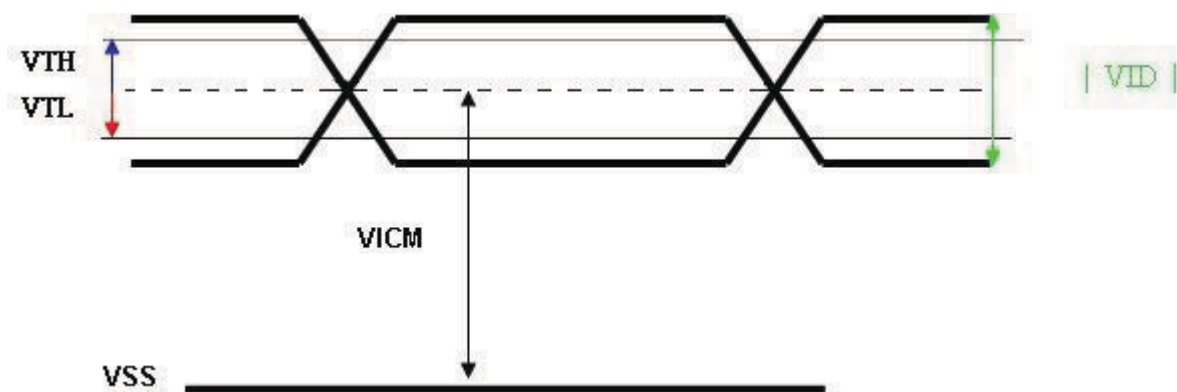
Symbol	Parameter	Min	Typ	Max	Units	Remark
VDD	Logic/LCD Drive Voltage	3.0	3.3	3.6	[Volt]	±10%
IDD	VDD Current	TBD	TBD	TBD	[mA]	64 Gray Bar Pattern (VDD=3.3V, at 60Hz)
Irush	LCD Inrush Current	-	-	3	[A]	
PDD	VDD Power	-	TBD	TBD	[Watt]	64 Gray Bar Pattern (VDD=3.3V, at 60Hz)

5.1.2 Signal Electrical Characteristics

Input signals shall be low or Hi-Z state when VDD is off.

Symbol	Item	Min.	Typ.	Max.	Unit	Remark
V _{TH}	Differential Input High Threshold	-	-	100	[mV]	V _{CM} =1.2V
V _{TL}	Differential Input Low Threshold	100	-	-	[mV]	V _{CM} =1.2V
V _{ID}	Input Differential Voltage	100	400	600	[mV]	
V _{ICM}	Differential Input Common Mode Voltage	1.15	1.2	1.45	[V]	V _{TH} /V _{TL} =±100mV

Note: LVDS Signal Waveform.



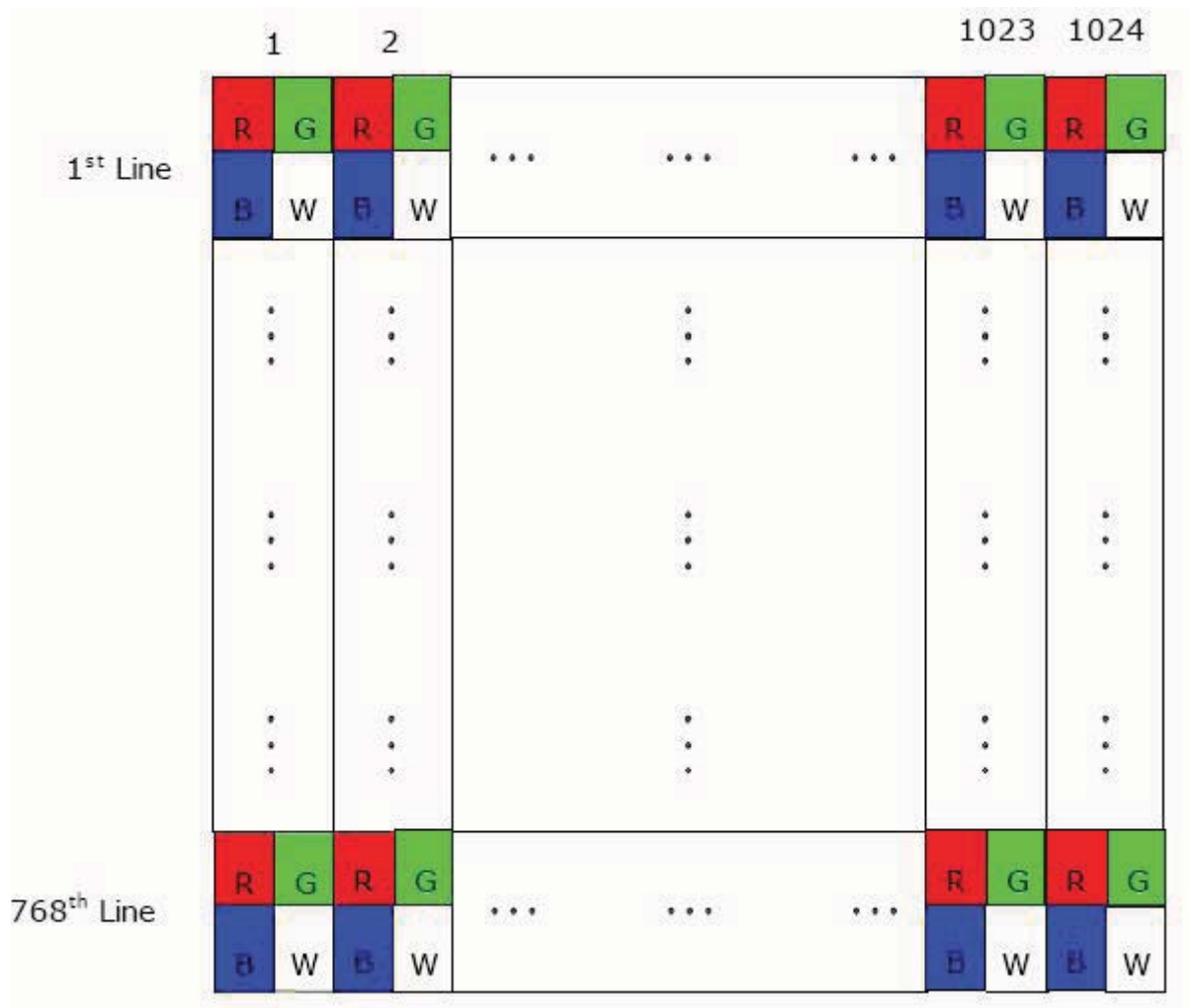
5.2 Backlight Unit

input 9V 350mA

6. Signal Characteristic

6.1 Pixel Format Image

Following figure shows the relationship between input signal and LCD pixel format.



6.2 Scanning Direction

The following figures show the image seen from the front view. The arrow indicates the direction of scan.

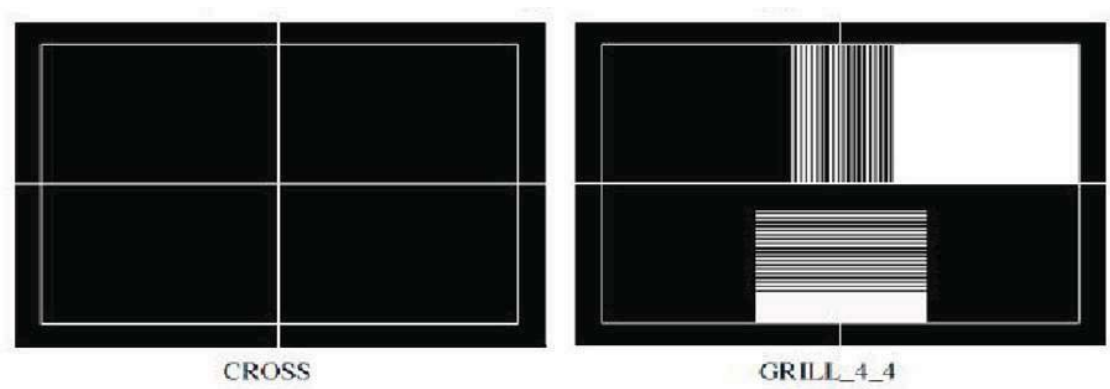


Fig. 1 Normal scan (Pin4, REV = Low or NC)

Fig. 2 Reverse scan (Pin4, REV = High)

6.3 Signal Description

The module using a pair of LVDS receiver SN75LVDS82(Texas Instruments) or compatible. LVDS is a differential signal technology for LCD interface and high speed data transfer device. Transmitter shall be SN75LVDS83(negative edge sampling) or compatible. The first LVDS port(RxOxxx) transmits odd pixels while the second LVDS port(RxExxx) transmits even pixels.

Input Signal Interface		
Pin No.	Symbol	Description
1	VDD	Power Supply, 3.3V (typical)
2	VDD	Power Supply, 3.3V (typical)
3	GND	Ground
4	GND	Ground
5	Rin0-	- LVDS differential data input (R0-R5, G0)
6	Rin0+	+ LVDS differential data input (R0-R5, G0)
7	GND	Ground
8	Rin1-	- LVDS differential data input (G1-G5, B0-B1)
9	Rin1+	+ LVDS differential data input (G1-G5, B0-B1)
10	GND	Ground
11	Rin2-	- LVDS differential data input (B2-B5, DE)
12	Rin2+	+ LVDS differential data input (B2-B5, DE)
13	GND	Ground
14	ClkIN-	- LVDS differential clock input
15	ClkIN+	+ LVDS differential clock input
16	GND	Ground
17	Rin3-	- LVDS differential data input (R6-R7, G6-G7,B6-B7) *Note2
18	Rin3+	+ LVDS differential data input (R6-R7, G6-G7,B6-B7) *Note2
19	NC	NC
20	GND	Ground

Note 1: Input signals shall be in low status when VDD is off.

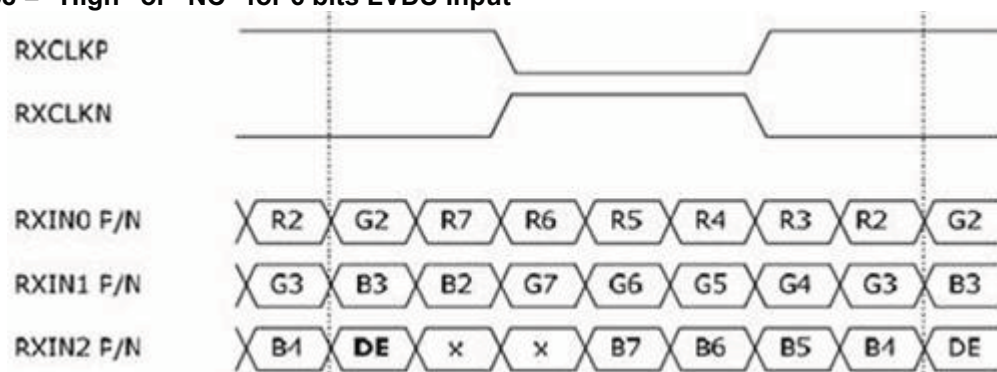
Note 2: If only the 6 bits mode is needed,

Note 3: High stands for “3.3V”, Low stands for “0V”, NC stands for “No Connection”.

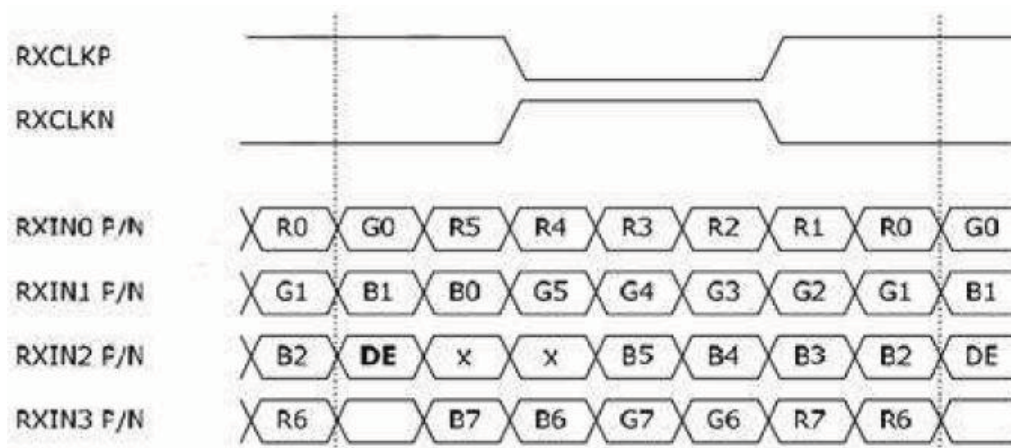
6.4 The Input Data Format

6.4.1 SEL68

SEL68 = "High" or "NC" for 6 bits LVDS Input



SEL68 = "Low" for 8 bits LVDS Input



Note1: Please follow PSWG.

Note2: R/G/B data 7:MSB, R/G/B data 0:LSB

Signal Name	Description	Remark
R7	Red Data 7	Red-pixel Data For 8Bits LVDS input MSB: R7 ; LSB: R0
R6	Red Data 6	
R5	Red Data 5	
R4	Red Data 4	
R3	Red Data 3	
R2	Red Data 2	
R1	Red Data 1	
R0	Red Data 0	
G7	Green Data 7	Green-pixel Data For 8Bits LVDS input MSB: G7 ; LSB: G0
G6	Green Data 6	
G5	Green Data 5	
G4	Green Data 4	
G3	Green Data 3	
G2	Green Data 2	
G1	Green Data 1	
G0	Green Data 0	

B7 B6 B5 B4 B3 B2 B1 B0	Blue Data 7 Blue Data 6 Blue Data 5 Blue Data 4 Blue Data 3 Blue Data 2 Blue Data 1 Blue Data 0	Blue-pixel Data For 8Bits LVDS input MSB: B7 ; LSB: B0 For 6Bits LVDS input MSB: B5 ; LSB: B0
RxCLKIN	LVDS Data Clock	The typical frequency is 65MHz. The signal is used to strobe the pixel data and DE signals. All pixel data shall be valid at the falling edge when the DE signal is high.
DE	Data Enable Signal	When the signal is high, the pixel data shall be valid to be displayed.

Note: Output signals from any system shall be low or Hi-Z state when VDD is off.

6.5 Interface Timing

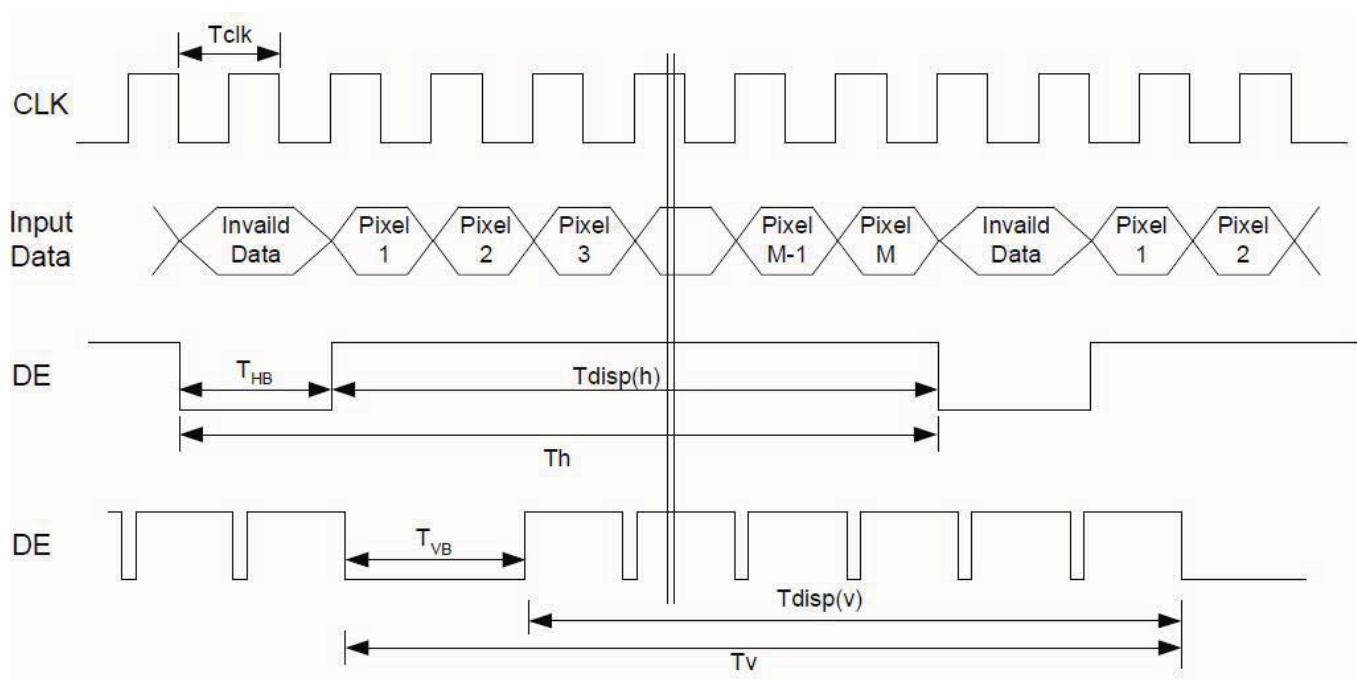
6.5.1 Timing Characteristics

Signal	Parameter		Symbol	Min.	Typ.	Max.	Unit
Clock Timing	Clock frequency		$1/T_{\text{Clock}}$	50	65	80	MHz
Vsync Timing	Vertical Section	Period	T_V	776	806	1023	T_{Line}
		Active	T_{VD}	-	768	-	
		Blanking	T_{VB}	8	38	255	
Hsync Timing	Horizontal Section	Period	T_H	1054	1344	2047	T_{Clock}
		Active	T_{HD}	-	1024	-	
		Blanking	T_{HB}	40	320	1023	

Note: DE mode only.

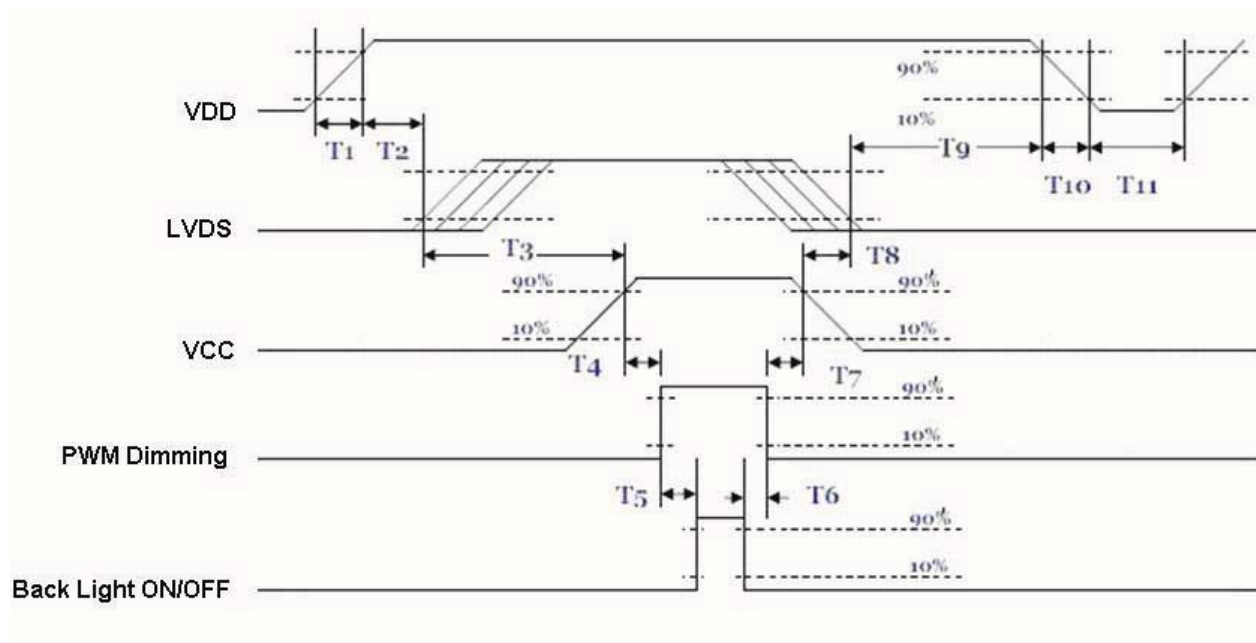
Note : Typical value refer to VESA STANDARD

6.5.2 Input Timing Diagram



6.6 Power ON/OFF Sequence

VDD power and LED on/off sequence is as below. Interface signals are also shown in the chart. Signals from any system shall be Hi-Z state or low level when VDD is off.



Power ON/OFF sequence timing

Parameter	Value			Units
	Min.	Typ.	Max.	
T1	0.5	-	10	[ms]
T2	30	40	50	[ms]
T3	200	-	-	[ms]
T4	10	-	-	[ms]
T5	10	-	-	[ms]
T6	0	-	-	[ms]
T7	10	-	-	[ms]
T8	100	-	-	[ms]
T9	0	16	50	[ms]
T10	-	-	10	[ms]
T11	1000	-	-	[ms]

The above on/off sequence should be applied to avoid abnormal function in the display. Please make sure to turn off the power when you plug the cable into the input connector or pull the cable out of the connector.

7. Connector & Pin Assignment

Physical interface is described as for the connector on module. These connectors are capable of accommodating the following signals and will be following components.

7.1 TFT LCD Module: LVDS Connector

Connector Name / Designation	Signal Connector
Manufacturer	STM or compatible
Connector Model Number	MSB240420-E
Mating Housing Part Number	P240420 or compatible

Pin#	Signal Name	Pin#	Signal Name
1	VDD	2	VDD
3	GND	4	REV
5	Rin0-	6	Rin0+
7	GND	8	Rin1-
9	Rin1+	10	GND
11	Rin2-	12	Rin2+
13	GND	14	CLKIN-
15	CLKIN+	16	GND
17	Rin3-	18	Rin3+
19	NC	GND	GND

8. Reliability Test Criteria

Items	Required Condition	Note
Temperature Humidity Bias	50°C/80%, 96 hours	
High Temperature Operation	70°C 96 hours	
Low Temperature Operation	-20°C 96 hours	
Hot Storage	80°C 96 hours	
Cold Storage	-30°C 96 hours	
Thermal Shock Test	-20°C/30 min ,60°C/30 min ,10cycles	

